C5.1

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T836/837 Fault Finding

Caution: This equipment contains CMOS devices which are susceptible to damage from static charges. Refer to Section 1.2 in Part A for more information on anti-static procedures when handling these devices.

The following test procedures and fault finding flow charts may be used to help locate a hardware problem, however they are by no means a complete fault finding procedure. If you still cannot trace the fault after progressing through them in a logical manner, contact your nearest Tait Dealer or Customer Service Organisation. If necessary, you can get additional technical help from Customer Support, Tait Electronics Ltd, Christch-urch, New Zealand (full contact details are on page 2).

- *Note 1:* In this and following sections deviation settings are given first for wide bandwidth sets, followed by settings in brackets for mid bandwidth sets () and narrow bandwidth sets [].
- *Note 2:* Unless otherwise specified, the term "PGM800Win" used in this and following sections refers to version 3.00 and later of the software.

Refer to Section 6 where the parts lists, grid reference index and diagrams will provide detailed information on identifying and locating components and test points on the main PCB. The parts list and diagrams for the VCO PCB are in Part E.

Section		Title	Page
5.1		Visual Checks	5.3
5.2		Component Checks	5.3
5.3		Front Panel LED Indicator	5.3
5.4		DC Checks	5.4
5.4	1 .1	Power Rails	5.4
5.4	4.2	VCO Locking	5.4
5.5		RF Checks	5.5
5.5	5.1	T836 Drive Power	5.5
5.5	5.2	T836 PA Output Power	5.5
5.5	5.3	T837 Output Power	5.5
5.5	5.4	Audio And Modulation	5.6
5.6		PGM800Win Generated Errors	5.7

The following topics are covered in this section

Section		Title	Page
5.7		Fault Finding Charts	5.8
	5.7.1 5.7.1.1 5.7.1.2 5.7.1.3	Microcontroller Basic Checks Serial Communications CTCSS Encode	5.8 5.8 5.9 5.10
	5.7.2	Regulator	5.11
	5.7.3	Synthesiser	5.12
	5.7.4	T836 Drive Amplifier	5.15
	5.7.5	T836 PA & Power Control	5.16
	5.7.6	T837 Exciter Drive Amplifier	5.17
	5.7.7	Audio Processor	5.18
5.8		To Replace The T836 PA Transistors (Q415 & Q425)	5.19

Figure	Title	Page
5.1	RF Diode Probe Circuit	5.5
5.2 Positioning Of Components Around Q425 & Q415		5.20

C5.3

5.1 Visual Checks

Remove the covers from the T836/837 and inspect the PCB for damaged or broken components, paying particular attention to the surface mounted devices (SMD's).

Check for defective solder joints. If repair or replacement is considered necessary, refer to Section 3 of Part A.

5.2 Component Checks

If you suspect a transistor is faulty, you can assess its performance by measuring the forward and reverse resistance of the junctions. Unless the device is completely desoldered, first make sure that the transistor is not shunted by some circuit resistance. Use a good quality EVM (e.g. Fluke 75) for taking the measurements (or a 20k ohm/V or better multimeter, using only the medium or low resistance ranges).

The collector current drawn by multi-junction transistors is a further guide to their performance.

If an IC is suspect, the most reliable check is to measure the DC operating voltages. Due to the catastrophic nature of most IC failures, the pin voltages will usually be markedly different from the recommended values in the presence of a fault. The recommended values can be obtained from either the circuit diagram or the component data catalogue.

5.3 Front Panel LED Indicator

The green "Supply" LED on the T836/837 front panel will flash according to the conditions described in the following table:

Flash Ra	Condition	
fast	(¹ / ₃ sec. on/ ¹ / ₃ sec. off approx.)	T836/837 is linked with PGM800Win
unequal	(¹ / ₃ sec. on/1 sec. off approx.)	microcontroller has detected an internal communications error -
		refer to Section 5.7.1

Where two or more conditions occur at the same time, the precedence is in the order shown above (i.e. T836/837 linked has the highest priority, followed by internal error).

5.4 DC Checks

5.4.1 Power Rails

Refer to the test points and options diagrams in Section 6 for test point locations, and to the regulator fault finding chart (Section 5.7.2) for fault diagnosis.

Check the 13.8V (TP601) and 9V (TP602) supplies at their test points in the regulator compartment with a DMM.

Check the 5V (TP604) and 20V (TP603) rails at their respective test points in the regulator compartment.

Check that Tx-Reg. (TP305 in the exciter compartment) comes up to 8.8V when the exciter is keyed.

Check the +5V digital regulator output (TP607 in the regulator compartment).

Check for short circuits.

5.4.2 VCO Locking

Key the exciter.

Using a DMM, monitor the VCO control voltage at PL4-1 or the junction of L1 and R1 on the VCO PCB.

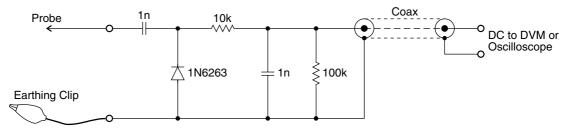
If the synthesiser is locked and the VCO aligned, the voltage at this point should be between 5 and 13V.

If the VCO is not locked, refer to the synthesiser fault finding chart (Section 5.7.3).

5.5 RF Checks

In-circuit RF levels may be measured with an RF probe on which the earth lead has been shortened to a minimum (i.e. 13mm). Refer to the circuit diagrams for typical levels.

Figure 5.1 shows a suitable RF probe circuit.



This unit is not suitable for use on high power RF circuits.

Figure 5.1 RF Diode Probe Circuit

5.5.1 T836 Drive Power

Refer to the drive amplifier fault finding chart (Section 5.7.4).

Ensure that the VCO locks (refer to Section 5.4.2).

Connect the drive output to a power meter and key the transmitter.

Check that the output power is between 600 and 1000mW.

Note: If the synthesiser is out of lock, the lock detector (synthesiser IC740 and comparator IC750) will prevent the RF signal from reaching the PA by switching the supply to the exciter amplifier (Q340, Q345).

5.5.2 T836 PA Output Power

Reconnect the drive output to the PA input.

Connect the PA to a power meter and key the transmitter.

Check that the output power is >30W with RV320 (power adjust) adjusted fully clockwise.

5.5.3 T837 Output Power

Refer to the exciter drive amplifier fault finding chart (Section 5.7.6).

Ensure that the VCO locks (refer to Section 5.4.2).

Connect the exciter output to a power meter and key the exciter.

Check that the output power is between 600 and 1000mW.

Note: If the synthesiser is out of lock, the lock detector (synthesiser IC740 and comparator IC750) will prevent the RF signal from reaching the PA by switching the supply to the exciter amplifier (Q340, Q345).

5.5.4 Audio And Modulation

Refer to the audio processor fault finding chart (Section 5.7.7).

Set up the audio processor as described in Section 3.9.

Check that the demodulated RF output has the frequency response referred to in Section 4.5 with at least \pm 5kHz (\pm 4kHz) [\pm 2.5kHz] deviation available at 1kHz modulating frequency.

If the above result is not achieved, either the two modulators are incorrectly adjusted or a fault condition exists.

C5.7

5.6 PGM800Win Generated Errors

The following errors are those most likely to occur using PGM800Win. Refer to the PGM800Win software user's manual for a complete list of error messages.

Channel Switch Set

The programmed default channel change was not accepted by the base station because a channel is selected externally. Try turning the external channel switch off to change the default channel in PGM800Win.

Synth Out Of Lock

The synthesiser received incorrect data, the data was corrupted, or a frequency outside the VCO switching range was entered. Enter a frequency within the VCO switching range, or tune the VCO.

Internal Error

Data could not be read from the base station due to an internal error. Check for shorts or open circuits on the SDA, SCK, SYNTH and EPOT lines. The SDA, SCK and SYNTH are normally high, and the EPOT is normally low.

Write/Read To An Unlinked Module

The link to the module does not exist. Undefined error. The link to the module has somehow been broken, possibly due to a temporary loss of the power supply to the module. Try relinking the module in PGM800Win (F9 key or the 'Link Module' option on the 'Communication' menu).

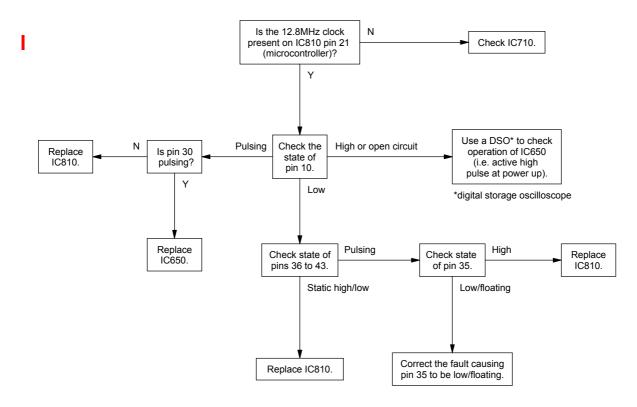
5.7 Fault Finding Charts

Note: The standard test point designations used in this section are as follows:

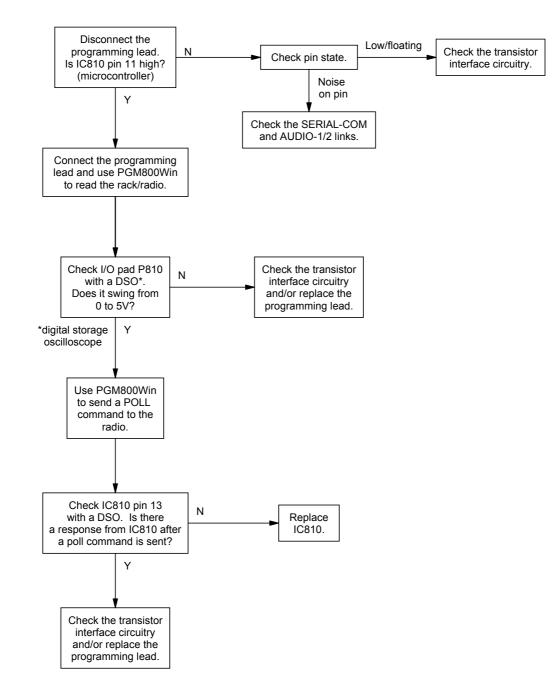
13.8V
9V
20V
5V

5.7.1 Microcontroller (IC810)

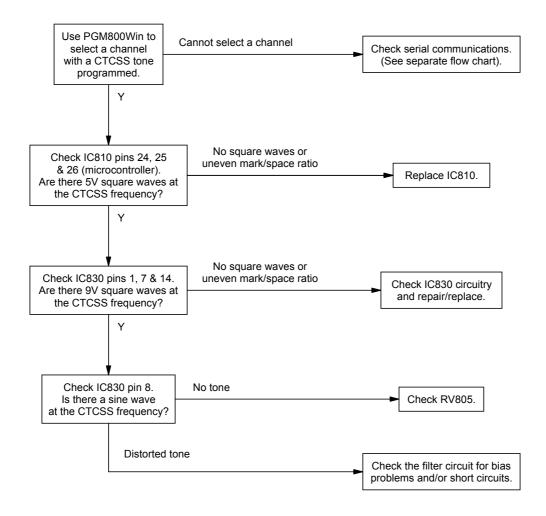
5.7.1.1 Basic Checks



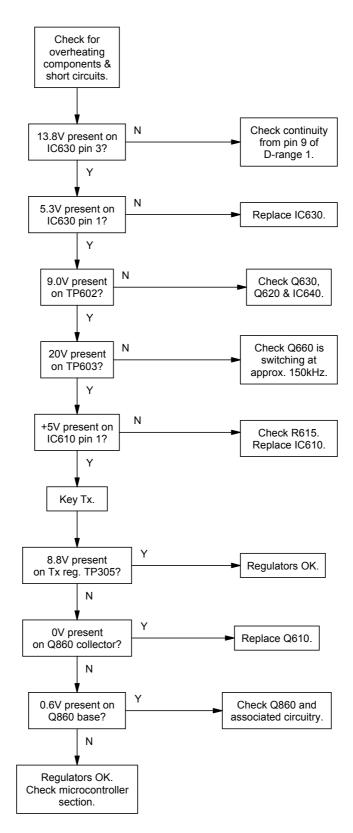
5.7.1.2 Serial Communication



5.7.1.3 CTCSS Encode

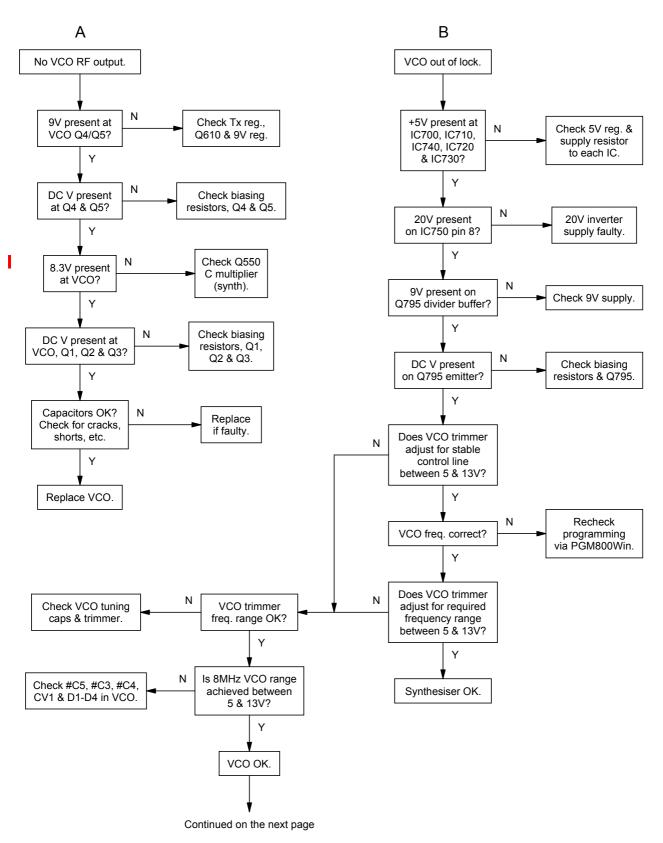


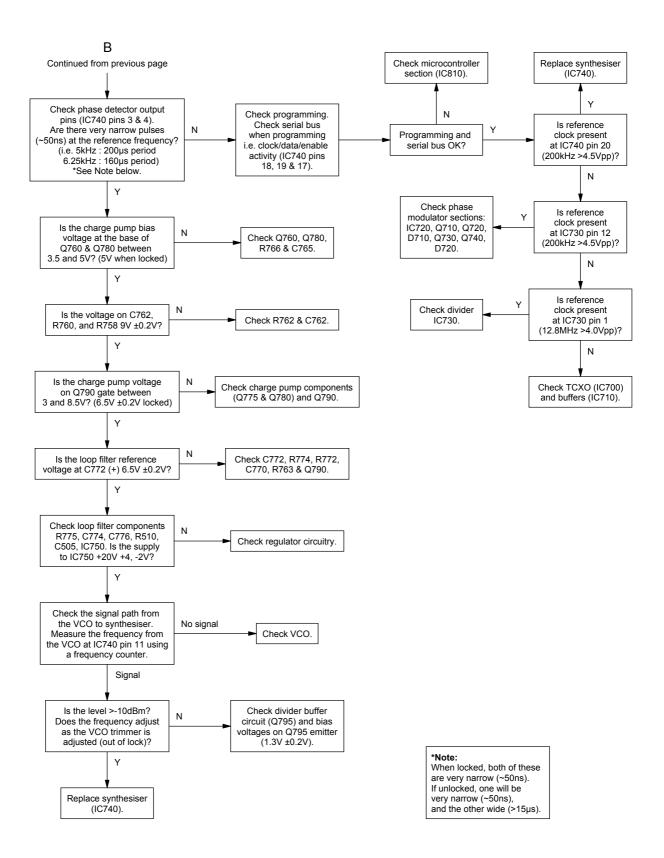
5.7.2 Regulator

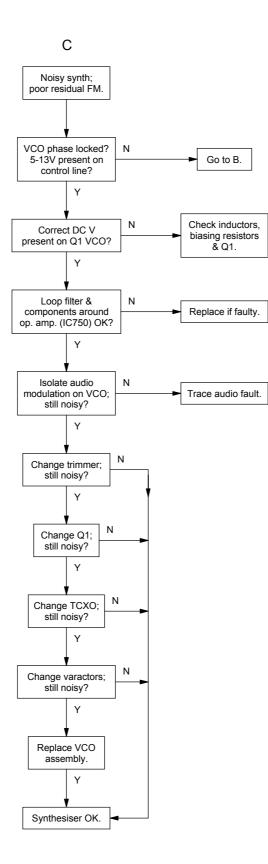


5.7.3 Synthesiser

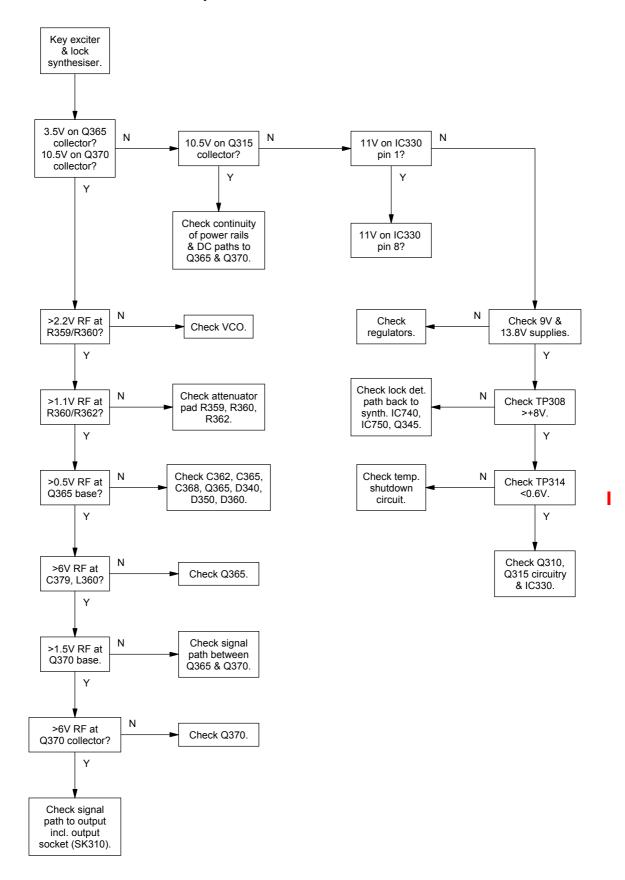
Refer to the synthesiser circuit diagram (sheet 7) in Section 6 and the VCO circuit diagram in Part E.

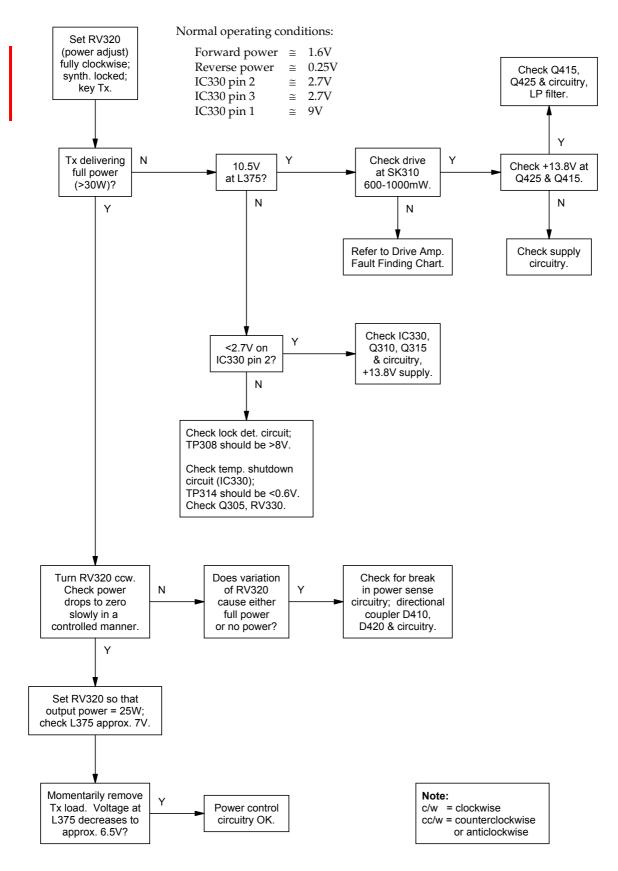




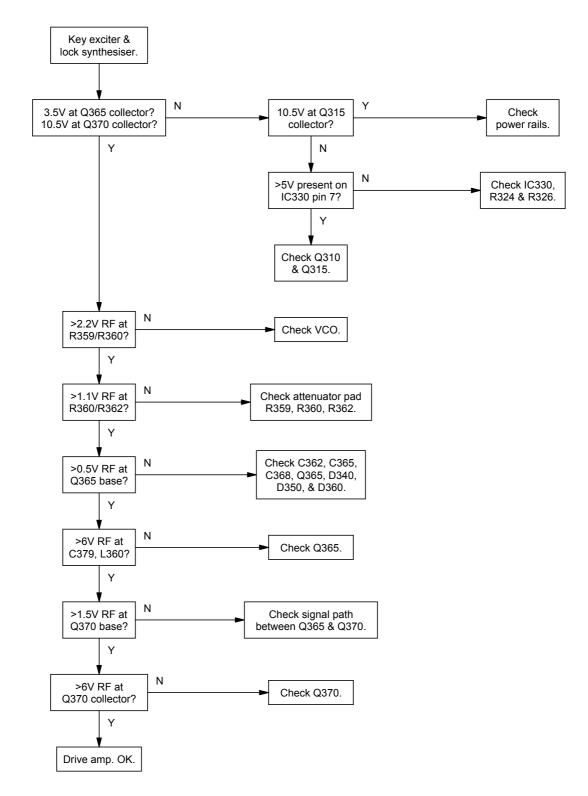


5.7.4 T836 Drive Amplifier

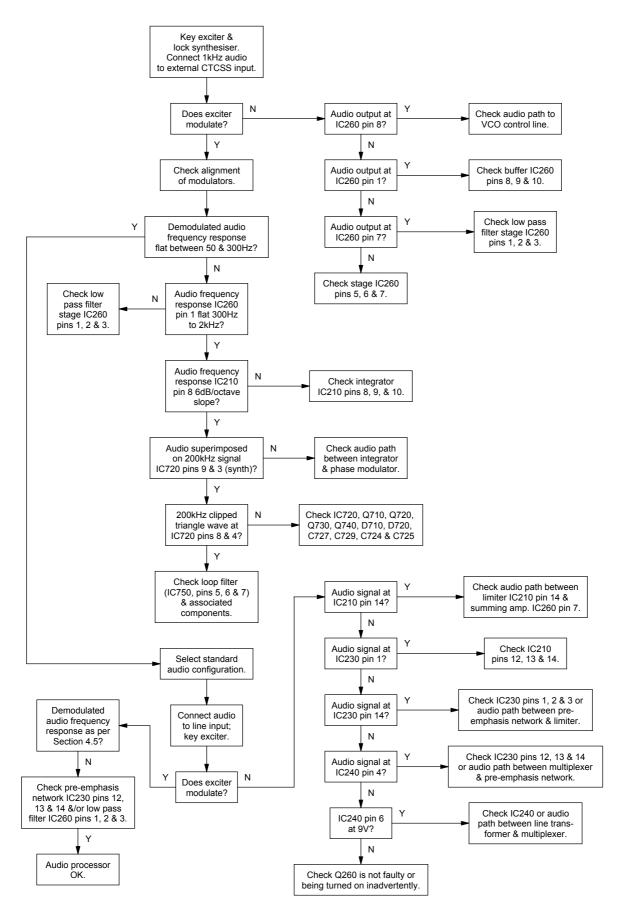




5.7.6 T837 Exciter Drive Amplifier



5.7.7 Audio Processor



5.8 To Replace The T836 PA Transistors (Q415 & Q425)

Caution: Failure to comply with the following procedure can result in failure of the device due to poor heatsinking, or worse, can endanger the health of the assembler if the beryllium oxide die carrier is smashed during assembly.

Caution: As the location of certain components in the PA is critical to performance, it is important that any components removed or disturbed are refitted in *exactly* the same position.

Caution: Before attempting to remove a transistor, note the position, type and value of each capacitor and resistor so that it can be replaced in *exactly* the same position (refer to Figure 5.2).

Caution: Do not apply too much heat or pressure to the PCB pads and tracks as you may damage them or lift them from the PCB, causing permanent damage to the transmitter.

Remove the resistors and/or chip capacitors from around the transistor.

Desolder the transistor tabs by heating with a soldering iron and lifting away from the PCB with a screwdriver or thin stainless steel spike. Unscrew the transistor stud nut and remove the device.

Remove any excess solder from the PCB pads with solder wick.

Trim the tabs of the replacement transistor so that the device sits neatly on the PCB pads provided.

Lightly tin the underside of the transistor tabs. Remove any excess solder to leave a thin, even layer on the tabs.

Apply a small amount of heatsink compound (Dow-Corning 340 or equivalent) to the transistor mounting surface. Sufficient compound should be used to ensure an even film over the entire mounting surface.

Place the transistor on the PCB in the correct orientation and ensure the tabs are flush to the surface. Lightly solder one tab to the PCB. Torque down the retaining nut to the correct torque (0.7Nm/6in.lbf.).

Caution:

Do not solder all the tabs before torquing down otherwise the device may be broken.

Solder all transistor tabs to the PCB.

Replace each resistor and/or capacitor in exactly the same position as noted previously.

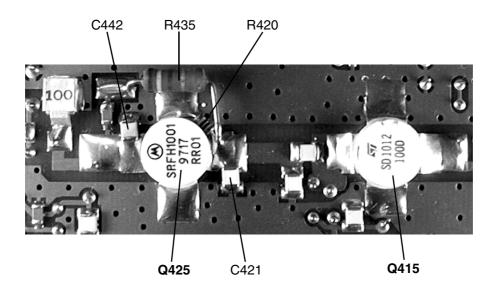


Figure 5.2 Positioning Of Components Around Q425 & Q415